

## **UG Electrical Engineering**

Sl.No.	Roll Number (June)/ (Dec)	Candidate Name	Father Name	Status
1.	172510/ 17800740	Yoginder Singh Malik	Kehar Singh Malik	Qualified*

\*The Candidate appeared in the degree Validation Examination conducted under the joint supervision of the UGC-AICTE as per the Hon'ble Supreme Court Judgement dated 03/11/2017 and have been declared qualified by rounding off marks scored by him i.e. 39.80% to 40% of marks honoring the verdict dated 04.11.2024 of the Hon'ble High Court of Delhi in the Writ Petition No. 1807 of 2024.